Docket No.

197802US2S

IN RE APPLICATION OF: Wakako MORIYAMA, et al

SERIAL NO: 09/670,520

FILED:

September 26, 2000

FOR:

METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICES USING THERMAL NITRIDE FILMS AS

GATE INSULATING FILMS

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

Transmitted herewith is an amendment in the above-identified application.

- No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith: Marked-up Copy of Amendment

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	,	CALCULATIONS
TOTAL	15	MINUS	22	0	x \$18	. =	\$0.00
INDEPENDENT	6	MINUS	6	0	x \$84	=	\$0.00
☐ MULTIPLE DEPENDENT CLAIMS + \$280 =						\$0.00	
,	TOTAL OF ABOVE CALCULATIONS					\$0.00	
☐ Reduction by 50% for filing by Small Entity						\$0.00	
11	☐ Recordation of Assignment			+ \$40	=	\$0.00	
a for the member of the same					TO	ΓAL	\$0.00

- \square A check in the amount of \$0.00 is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

J. Maier

Registration No. 25,599

David A. Bilodeau

Registration No. 42,325

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Customer Number 22850 Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 10/01) I:\atty\DAB\197802US Amend Cvr.doc 197802US-2S



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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

WAKAKO MORIYAMA ET AL

: EXAMINER: LEE, G. D.

SERIAL NO: 09/670,520

FILED: SEPTEMBER 26, 2000

: GROUP ART UNIT: 2825

FOR: METHOD FOR MANUFACTURING

SEMICONDUCTOR DEVICES

USING THERMAL NITRIDE FILMS AS GATE INSULATING FILMS

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In response to the Official Action mailed December 17, 2002, please amend this application as follows:

IN THE SPECIFICATION

Please amend the specification to read as follows:1

Page 6, lines 2-10, please replace the paragraph with the following text:

According to an aspect of the present invention, there is provided a semiconductor device manufacturing method comprising forming a gate insulating film in an oxynitride form on a main surface of a semiconductor substrate; forming gate electrodes on the gate insulating film; removing the gate insulating film except under the gate electrodes to expose

¹A marked-up copy of the changes made to the specification is attached.